

	L #	Hits	S arch T xt	DBs
1	L1	3174	(257/691,723,724,777,924).ccls.	USP AT; US-P GPU B
2	L2	1939	(361/760,763,782,783).ccls.	USP AT; US-P GPU B
3	L3	4903	1 2	USP AT; US-P GPU B
4	L4	163	3 and @pd>=20030808	USP AT; US-P GPU B
5	L5	1	4 and (charg\$3 with pump\$3)	USP AT; US-P GPU B
6	L6	7	4 and (doubler booster multiplier pump\$3)	USP AT; US-P GPU B
7	L7	25	4 and ((doubl\$3 boost\$3) not 6)	USP AT; US-P GPU B

	L #	Hit	arch Text	DB
8	L8	6	7 and (inductor\$1 inductance\$1 coil\$1)	USP AT; US-P GPU B
9	L9	19	7 not 8	USP AT; US-P GPU B
10	L10	214	3 and ((voltage current) with (doubl\$3 boost\$3 multipl\$4 pump\$3))	USP AT; US-P GPU B
11	L11	106	10 and (inductor\$1 inductance\$1 coil\$1)	USP AT; US-P GPU B
12	L12	46	11 and (bump\$2 ball\$1)	USP AT; US-P GPU B
13	L14	60	11 not 12	USP AT; US-P GPU B
14	L15	2	10 and (inductive adj element\$1)	USP AT; US-P GPU B